

a thermal resistor connected to said diode and said base of said first triode;
a second resistor connected between said base and said collector of said first triode;

a second triode having a base and a collector and an emitter;

a third resistor, said emitter of said first triode being connected to said negative power supply terminal, said collector of said first triode being connected to said third resistor and connected to said base of said second triode; and

a fourth resistor connected between said emitter and said collector of said second triode so as to send speed signals to said fan activation IC.

6. (new) The circuit of Claim 5, said diode being rated at 5.1 volts.

7. (new) The circuit of Claim 5, said thermal resistor being a negative temperature thermal resistor.